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DOCKET NO. SC91051A

## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPLICANT(S) Supamas Sirichotiyakul et al. GROUP ART UNIT: 2123  
APPLN. NO.: 09/580,854 EXAMINER: Kandasamy Thangavelu  
FILED: May 30, 2000  
TITLE: METHODS FOR ANALYZING INTEGRATED CIRCUITS AND  
APPARATUS THEREFOR

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1-5-04

Signature

Pat Thomas

Pat Thomas

Printed Name of Person Signing Certificate

AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated October 3, 2003, Paper No. 4, and Examiner's  
comments with regard thereto, please enter the following amendments in the above-entitled  
application, without prejudice or disclaimer.

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**15**

Number of Pages (including this page)

Date: January 5, 2004

To: Kandasamy Thangavelu - 2123

Location: United States Patent and Trademark Office

Fax No.: (703) 872-9306

From: Joanna G. Chiu - 43,629

Subject: 09/580,854- Supamas Sirichotiyakul et al.

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**MESSAGE:**

Enclosed herewith, please find a FORMAL AMENDMENT for filing in the below-identified application. If Applicant has overlooked any fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit 502117.

**ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:**

1.	x	1 page Facsimile Cover Sheet
2.	x	14 page Amendment

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EXAMINER: Kandasamy Thangavelu  
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SERIAL NO.: 09/580,854  
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INVENTOR: SUPAMAS SIRICHOTIYAKUL ET AL.